Synthesis of Computational Structures for Analog Signal Processing

Cosmin Radu Popa

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Synthesis of Computational Structures for Analog Signal Processing focuses on analysis and design of analog signal processing circuits. The author presents a multitude of design techniques for improving the performances of analog signal processing circuits, and proposes specific implementation strategies that can be used in CMOS technology. The author's discussion proceeds from the perspective of signal processing as it relates to analog. Included are coverage of low-power design, portable equipment, wireless nano-sensors and medical implantable devices.

The material is especially appropriate for researchers and specialists in the area of analog and mixed-signal CMOS VLSI design, as well as postgraduate or Ph.D. students working on analog microelectronics.

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